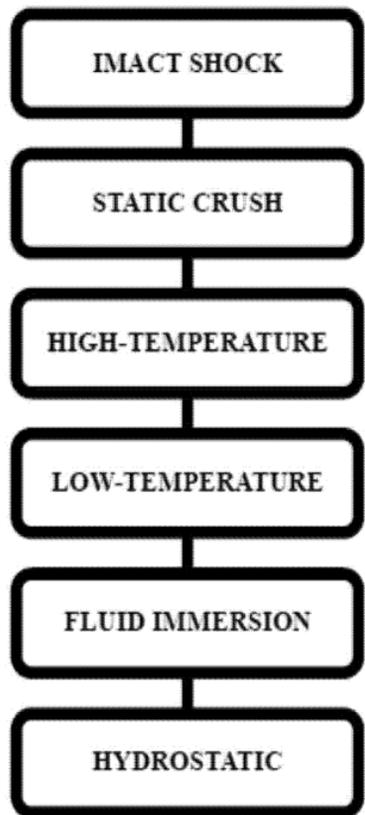
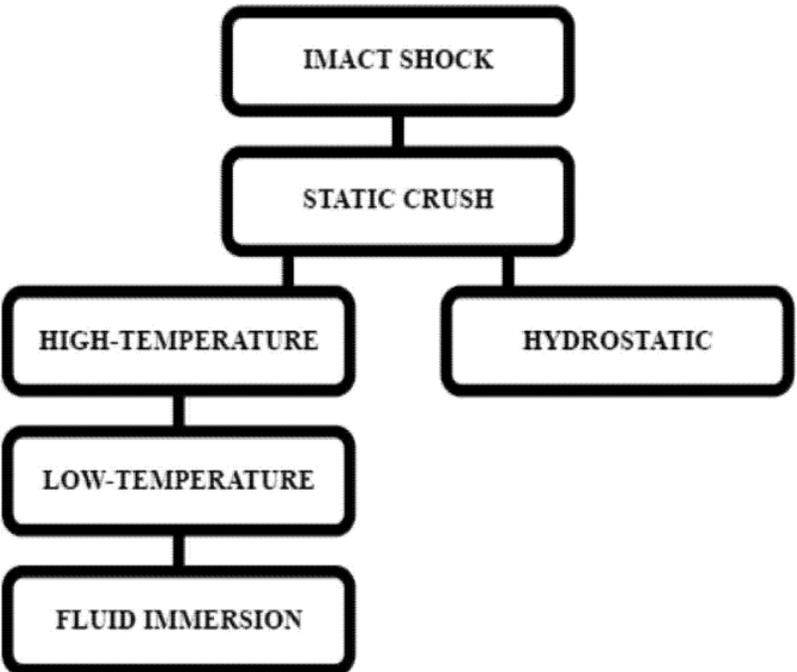


Figure 1 to Appendix A to Part 299 – Event Recorder Memory Module Sequential Testing



SINGLE BRANCH SEQUENTIAL TESTING



SPLIT BRANCH SEQUENTIAL TESTING